EE4902  ANALOG IC DESIGN FLOW

1. SYSTEM REQUIREMENTS
2. DESIGN SPECIFICATIONS
3. ELECTRICAL DESIGN (SCHEMATIC)
4. SIMULATION
5. COMPARE WITH SPECIFICATIONS
   - FAIL
   - REDESIGN
   - FAIL
   - COMPARE WITH SCHEMATIC
     - PASS
     - PARASITIC EXTRACTION
     - SIMULATION
     - COMPARE WITH SPECIFICATIONS
       - PASS
       - FABRICATION
       - FINAL TEST TO SPECIFICATIONS
         - FAIL
         - PASS
         - PRODUCT

   - PASS